



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/033,142	11/01/2001	Dale E. Gulick	2000.051500	7068

23720 7590 03/29/2005

WILLIAMS, MORGAN & AMERSON, P.C.
10333 RICHMOND, SUITE 1100
HOUSTON, TX 77042

EXAMINER

AUVE, GLENN ALLEN

ART UNIT	PAPER NUMBER
----------	--------------

2111

DATE MAILED: 03/29/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.		Applicant(s)	
	10/033,142		GULICK, DALE E.	
	Examiner		Art Unit	
	Glenn A. Auve		2111	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 18 January 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-48 is/are pending in the application.
- 4a) Of the above claim(s) 19-23,30 and 31 is/are withdrawn from consideration.
- 5) ☒ Claim(s) 40-48 is/are allowed.
- 6) ☒ Claim(s) 1-3,5-9,11-18,24,25,27-29,32,33 and 35-38 is/are rejected.
- 7) ☒ Claim(s) 4,10,26,34 and 39 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Applicant should note that the examiner in charge of this application has changed. The new contact information is included at the end of this action.

Election/Restrictions

2. This application contains claims 19-23,30, and 31 drawn to an invention nonelected without traverse. A complete reply to the final rejection must include cancellation of nonelected claims or other appropriate action (37 CFR 1.144) See MPEP § 821.01.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1-3, 6-8, 11-18, 24, 25, 27-29, 32, 33, 35-38 are rejected under 35 U.S.C. 102(e) as being anticipated by Lindsay et al., 2002/0194415 A1 (hereinafter Lindsay).

- a. As to claims 1-3, 6-8, 11, 24, 25, 27, 33, Lindsay discloses an apparatus comprising an integrated circuit configured as a bridge (note Figure 10, element 1020 and Figure 11, element 1120, wherein the multiprotocol bus interface adapters 1025 and 1125 in Figures 10 and 11, respectively, bridge the SMBus and PCI/PCI-X buses -- note [0107-0110]), wherein the integrated circuit comprises an internal bus (note Figure 8, 811); a microcontroller

(note Figure 8, 825) connected to the internal bus, wherein the microcontroller is configured to master the internal bus (i.e., inherent); an Ethernet controller (note Figure 8, 808) coupled to the internal bus, wherein the Ethernet controller and the microcontroller are configured to exchange data over the internal bus; and a plurality of buffers (note Figure 8, 818 and buffers included within MAC 808) coupled between the microcontroller and the Ethernet controller for buffering the data, wherein the plurality of buffers are connected between the internal bus and the Ethernet controller, wherein the microcontroller (i.e., alert supervisory bus controller) is configured as an Alert Standard Format master, and wherein the Ethernet controller is configured to route Alert Standard Format messages to the microcontroller (note Figures 7 and 8, abstract, [0083-0085]), further comprising: a status register configured to store Alert Standard Format sensor data, wherein the Alert Standard Format sensor data is stored in the status register by the microcontroller (note Figure 7, 724 and Figure 8, 824), further comprising: a power port configured to receive a reserve power signal, wherein the reserve power signal provides reserve power to the status register configured to store Alert Standard Format sensor data (i.e., inherent to ASF, ACPI – [0075]), that the bridge further includes a first bus interface logic for coupling to a first external bus (i.e., PCI/PCI-X buses) and a second bus interface logic for coupling to a second external bus (i.e., SMBus) (note Figures 10 and 11 and [0107-0110], wherein it is inherent that the bridge includes first and second bus interface logic, since the bridge allows for communication with the first bus and the second bus), further comprising: a remote management and control protocol set command unit connected to the internal bus, wherein the remote management and control protocol set command unit is configured (note [0080]).

b. As to claims 12-16, Lindsay discloses that the integrated circuit further comprises a memory connected to the internal bus, wherein the memory includes a read-only memory,

wherein the memory includes random access memory, wherein the random access memory is configured to shadow a read-only memory, wherein the random access memory is loaded during a boot-up process (note Figure 9, [0101]).

c. As to claims 17, 18, 28, 29, 37, and 38, Lindsay discloses that the microcontroller is configured to manage security in a computer system and the microcontroller is configured to manage health status of a computer system (note [0074, 0076]).

d. As to claim 32, the claimed elements have already been discussed with respect to claim 1 above, with the exception of an external bus and a processor coupled to the external bus, wherein the processor is configured to communicate over a network using the Ethernet controller.

Lindsay discloses an external bus (note Figures 7, bus 702 and Figure 8, bus 802) and a processor (i.e., inherent to computer system) coupled to the external bus, wherein the processor is configured to communicate over a network using the Ethernet controller.

e. As to claims 35 and 36, the claimed elements have already been discussed with respect to claims 1 and 32 above, with the exception of a computer system comprising bus interface logic coupled to the external bus and one or more sensors coupled to the external bus, wherein the Ethernet controller is configured to transmit data from the sensors over a network or that the microcontroller is configured to poll the sensors over the network.

Lindsay discloses that the computer system comprises bus interface logic (i.e., inherent to communicate over bus) coupled to the external bus and one or more sensors (note Figure 10, sensors 1040, 1050, 1045) coupled to the external bus, wherein the Ethernet controller (note Figure 10, 1020) is configured to transmit data from the sensors over a network (note Figure 10, 1015) and that the microcontroller is configured to poll the sensors over the network (note Figure 10 and [0107]).

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lindsay et al., 2002/0194415 A1 (hereinafter Lindsay) in view of what was well known in the art, as exemplified by any one of Hwang 6,516,398 and Ma et al., 6,182,235 (hereinafter Ma) and Dea et al., 5,742,833 (hereinafter Dea).

Lindsay fails to disclose that the microcontroller is further configured as an embedded 8051 microcontroller.

Examiner takes Official Notice that embedded 8051 microcontrollers are well known in the art for controlling a variety of devices, including appliances and computer devices, which include NICs, evidence of which may be found in:

Hwang at column 1, lines 27-44

Ma at column 1, lines 11-31

Dea at column 4, lines 1-14.

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the use of an embedded 8051 microcontroller in the system of Lindsay so as to take advantage of its widely recognized small size, low power consumption and versatility.

7. Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lindsay et al., 2002/0194415 A1 (hereinafter Lindsay) in view of Applicant's Admitted Prior Art (hereinafter AAPA).

As to claim 9, Lindsay fails to disclose that the bridge is configured as a south bridge, but does disclose that the bridge is part of a network controller (note [0020, 0034, 0107-0110]), and would therefore have to be configured as the bridge that is coupled to the network.

AAPA discloses that the bridge coupled to the network is the south bridge (note Figure 1A, south bridge 112).

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to configure the bridge of Lindsay as a south bridge, since, as AAPA teaches, the south bridge is coupled to the network. And coupling the bridge of Lindsay, along with its network control capabilities, would allow the system of Lindsay to remotely monitor and manage a client computer on the network, as Lindsay teaches in [0107-0110].

Response to Arguments

8. Applicant's arguments filed 18 January 2005 have been fully considered but they are not persuasive. Applicant only really argues that Lindsay does not show the buffers coupled between the microcontroller and the Ethernet controller. However, as noted in the description of figure 8 in paragraph [0083], the buffers in Lindsay are used to temporarily store packets traversing the bus during communication and processing. Therefore, the packets being transferred between the microcontroller and Ethernet controller can be stored in the buffers, and so the buffers can fairly be interpreted as being "between" these two elements since the buffers store data being transferred between them.

Allowable Subject Matter

9. Claims 40-48 are allowed.
10. Claims 4, 10, 26, 34, and 39 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

11. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

12. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Glenn A. Auve whose telephone number is (571) 272-3623. The examiner can normally be reached on M-F 8:00 AM-5:30 PM, every other Friday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on (571) 272-3632. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2111

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to read 'Glenn A. Auve', with a stylized flourish at the end.

Glenn A. Auve
Primary Examiner
Art Unit 2111

gaa
28 March 2005